OSISOFE USERS CONFERENCE **2009** SAN FRANCISCO

Centralized Remedial Action Scheme

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Using Emerging Telecommunication, Protection Technologies, and OSIsoft PI System to create a high performance wide area control system

> Empowering Business in Real Time PI Infrastructure for the Enterprise

To be covered

- What makes this project "special"
- Overview of the project
- Where is the benefit in using the PI System
- Other enhancements being discussed
- Interesting technical tidbits



What makes the project "special"

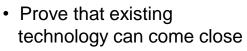




- Education
- Give information to the industry.
- Talk about benefits.
- Have authoritative test results.

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- Education
- Incremental technological developments.
- Project plan with multiple decision gates.
- Have a leader that "herds the cats".
- Have a team approach.
- No stupid questions.



- Centralize supervisory control/automation
- System wide decision optimization
- Use of communications
- Design and use of natural testing
- Use PI for SOE
- Make PI integral in testing/ problem correction cycle
- Don't be afraid to ask for help



- Distributed Automation projects
- Regional/cell based decisions.
- Hardware intensive
- Intermittent manual testing
- No SOE capture
- "Can't tell why it didn't work."
- 80-100 msec control

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Teamwork: Reaching the finish line

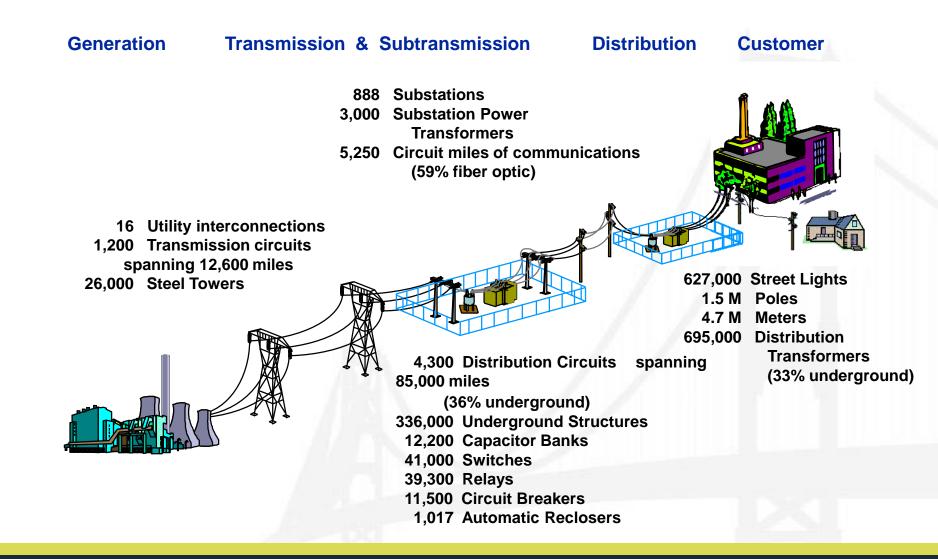




The project

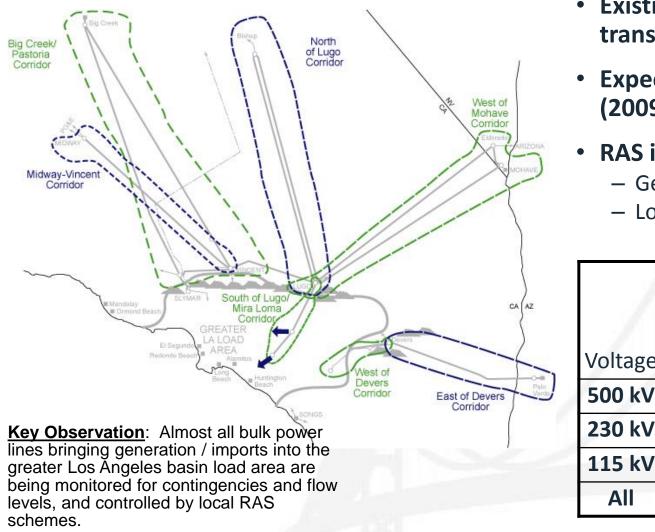


SCE T&D Assets



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SCE Transmission Corridors and Proliferating RAS Schemes



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- Existing RAS = 18 on all transmission corridors
- Expected potential new RAS (2009-2011) = 50-60
- RAS impacted transmission
 - Generation tripping
 - Load shedding

1	Miles of Transmission Circuits					
Voltage	Total Miles	RAS Monitored (%)				
500 kV	1,183	1,069 (90%)				
230 kV	3,574	1,181 (33%)				
115 kV	1,846	350 (19%)				
All	6,603	2,600 (40%))				

Project Objectives

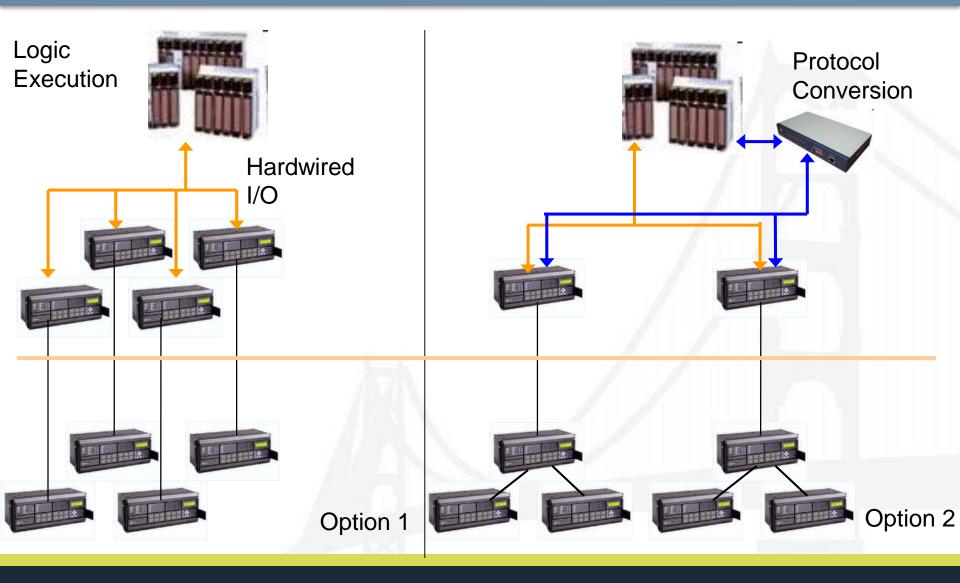
• Solves today's RAS problems:

- ONE SIZE FITS ALL: Inability to size a RAS driven mitigation targets based on dynamic assessment of generation tripping / load shedding requirements
- OVERLAP: Same Generation / Load subject to interruption for numerous reasons controlled by different RAS' and other reliable and safe operational requirements
- TIME LOSS: Excessive travel time by engineering and field staff to maintain the local RAS schemes at numerous sites
- CONTROLLER TECHNOLOGY LIMITATIONS: Inability to represent greater than 24 contingencies per controller
- Adopts Emerging Technologies to achieve higher performance
 - IEC61850 GOOSE Standard
 - OSIsoft PI System

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• Save money, decrease energy usage, increase testability and process improvement, and achieve higher morale

How "C-RAS/SPS" is typically done



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SCE C-RAS and comparison

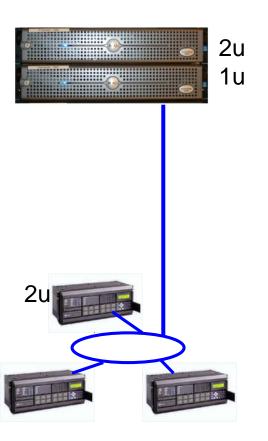
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For 80 Substations (5 devices per substation per system):

		Option 1	Option 2	<u>SCE</u>	Estimated Savings	
	# substation relays					
	A system	400	480	400	\$ 0-320K	
	B system	400	480	400	\$ 0-320K	
	# control center relays					
	A system	400	80	0	\$ 320K – 1.6M	
	B system	400	80	0	\$ 320K – 1.6M	
	# logic processors/scheme					
	A system	1	1/10	1/80	\$ 70K - 800K	
	B system	1	1/10	1/80	\$70K - 800K	
	# history captured					
		no	no	yes		

NCE

SCE – number of 19" racks required



option 1 option 2 SCE

racks A 40-80 20-40 2 B 40-80 20-40 2

Decreased floor space and lower requirements means not having to build two new control centers. **Savings = \$20M-40M**

Less heat and less computers is a more energy efficient solution.



Other benefits/observations

- Decreases overall telecom maintenance costs.
- SCE has fiber to most of its substations, need to "light it up".
- Easier to maintain and diagnose.
- Decreases time to deployment (from 2-3 years to 6months*)
 - Morale benefits and large savings.

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More benefits

- Use of IEC 61850 GOOSE allows for equipment from different manufacturers to be used within a single system.
 - Option 1 and Option 2 RAS schemes don't allow this.



Increased performance

- Option 1 and 2 have "local" performance of 20-30 msec.
- SCE pilot has an observed "local" performance of < 1 msec.



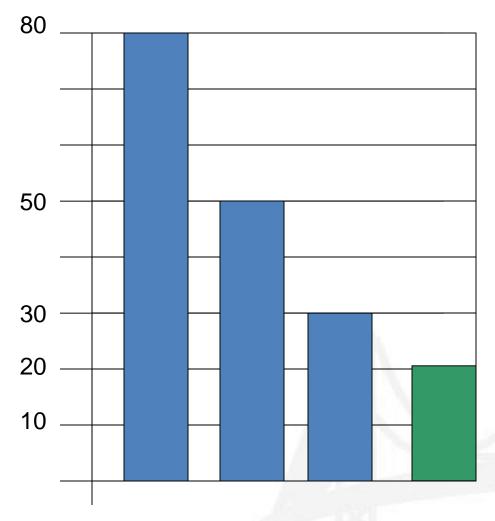
RAS Timeline

A RAS Case Simulation
 1
 2
 3
 4
 5A
 5B

 0
 1
 2
 3
 4
 5
 4
 5
 4
 5
 6
 7
 8
 9
 10
 11
 12
 13
 14
 15
 1 cycle = 16.7 milliseconds Time in Cvcles Time Operational Events Step 1 @ 0 Cycle 3 Phase Fault on the Bus Relay Processing time for Step 2 @ 1 Cycle trip signal to CBs Event Detection Fault 5 Cycles Clearing: Open CBs for Step 3 @ 5 Cycles line/transformer out Local RAS Logic Processing for Processing Step 4 @ 7 Cycles trip signal to CBs to trip 2 Cycles **RAS Processing:** generators time Open CBs associated with @ 10 Step 5A 12 generators (I Batch Cycles Mitigation) Open CBs associated with @ 12 Step 58 4 generators (II Batch Mitigation Generation Cycles Mitigation) Tripping / Load Shedding: 9 Cycles Open CBs associated with @ 16 Step 5C 2 generators (III Batch Cycles Mitigation) 16 Cycles Total Elapsed Time:

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C-RAS Performance vs. Potential Savings



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- Provides supervisory protection as needed by most RAS schemes
- Allows more complex algorithms to be created that provide a more optimal solution and less customer outages.
- Allows transmission lines to be loaded closer to the limits. Allows more low cost power to be imported.
- Performance of SCE system should allow deferral of generator builds.
 Increases system import capability by 5% (~800 MW).

The Benefits of PI



Project uses the PI System for:

• Typical Uses

- Archive/SOE
- Visualization
- Data Mining/Report
 Generation
- Design Compliance checking

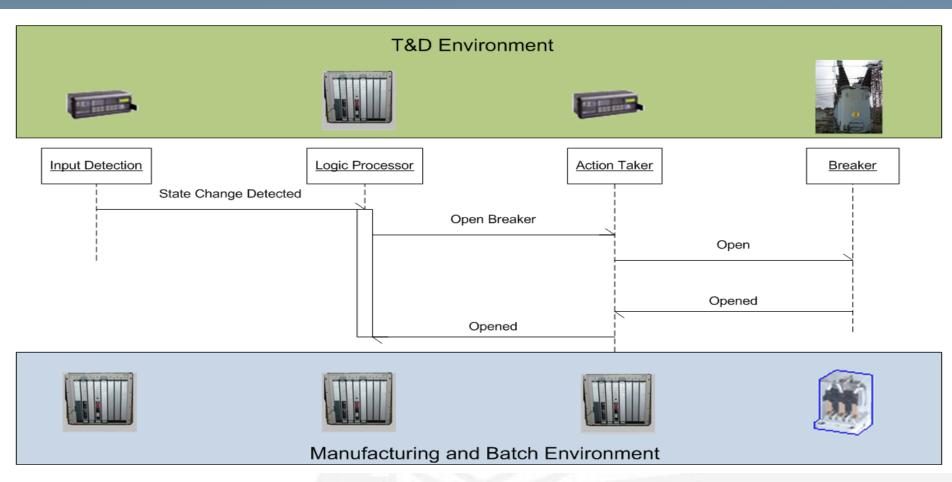
- Expanded use
 - System degradation and operation detection ability
 - Detecting an "operation" within past year.
 - Decreasing test and process improvement time.

Detecting an operation

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- The Western Electricity Coordinating Council (WECC) requires one (1) end-to-end test/operation per year.
- Ability to avoid this "outage"/decrease in availability is key.
- Design of system even lowers the costs should an end-to-end test be required.

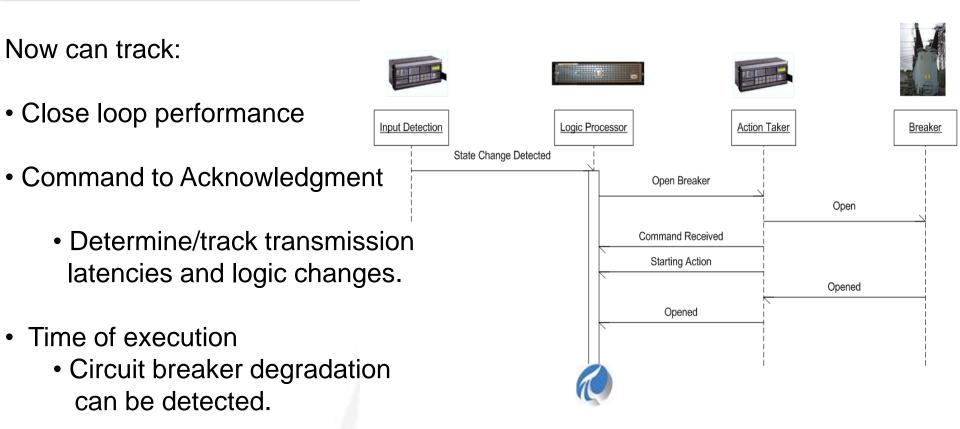
Typical Design of Automation Systems



Logic processor has little or no historical storage capability Several key process steps/timing are unknown



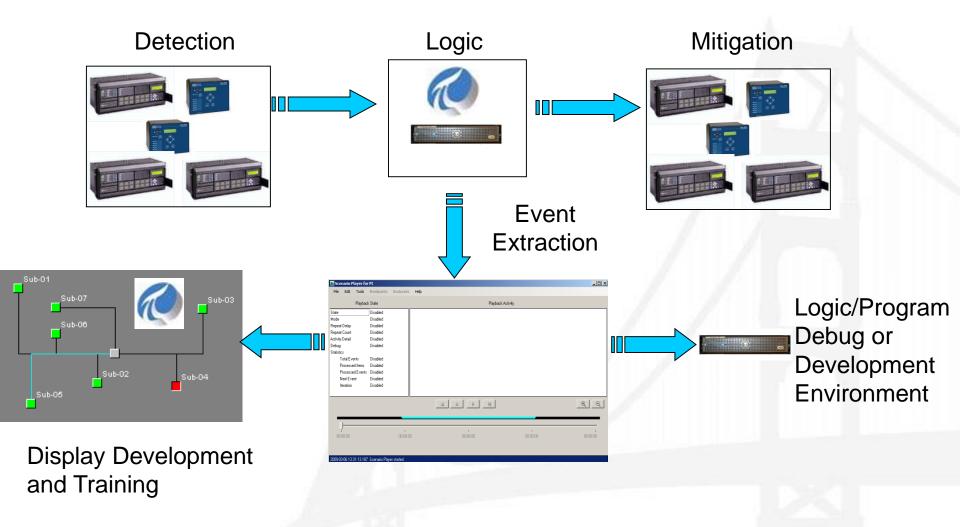
The SCE and natural testing approach



Fallout of approach: Can data mine/report for the last true operation within one (1) year and determine if an end-to-end test is needed.



Decreasing process improvement time



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What might be next



- Dynamic mitigation strategies
 - Integration of PMU
 (Phasor Measurement Unit) measurements
 - Real-time phase difference calculation



Technical Tidbits



Performance Design

- Needed to determine what is a worst case event
- Determine if an interface could be constructed to support communication requirements
- Prove that OSIsoft PI System can handle the worst case event.

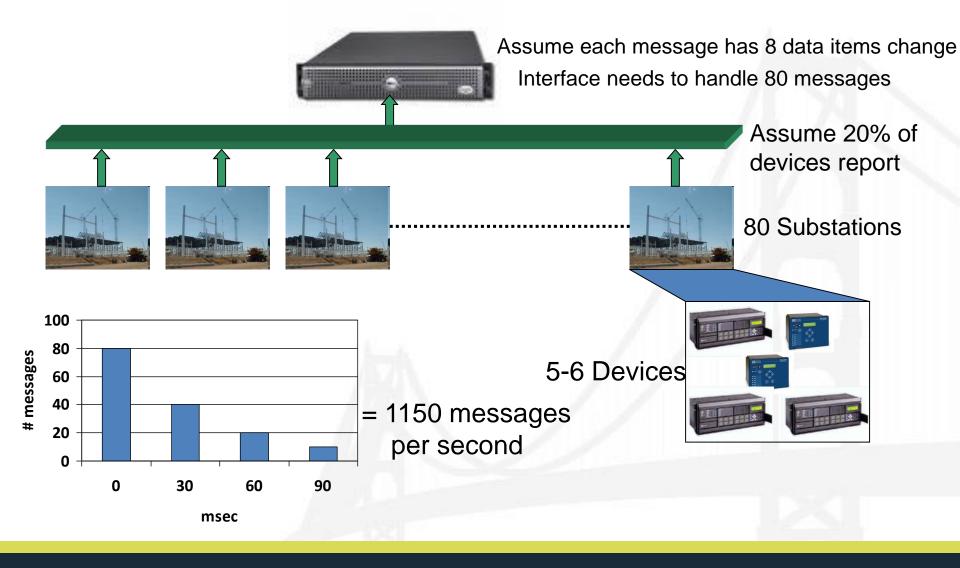


What is a field event?

- Can be characterized as a burst (e.g. not a continuous stream).
- The burst will subside, but may change characteristics based upon field actions.
- Amount of data that changes will be "large" initially and then decrease.

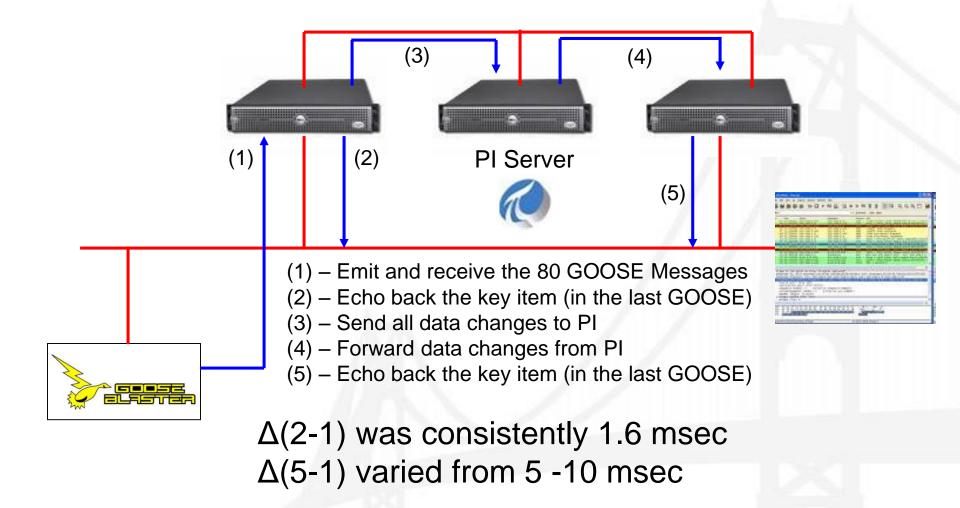


Assumption: 640 data changes/10 msec



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Test Set-up



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- Was able to be decreased by understanding interaction and threading model.
- Could be instrumented with PI performance counters and Windows Performance monitor.
 - Recommend PI users get familiar with these counters...

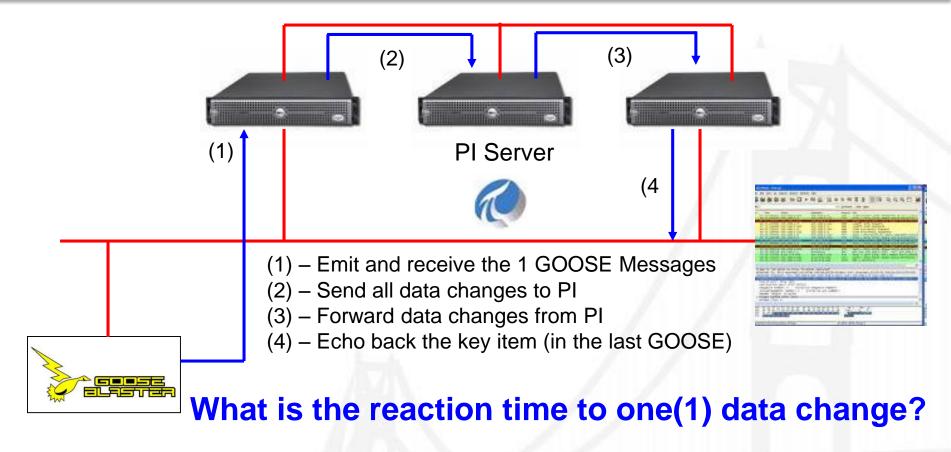


The importance?

- Allows PI to be used so that analytics can consume information from other interface nodes and still meet the 50 msec criteria.
- The design criteria of coordinating 80 substations has been increased to 880 (potentially).



Enquiring minds want to know:



Answer: 1-7 msec



Summary

OSIsoft PI System allows to accomplish

- High Performance
- Large \$\$\$ Savings
- Maintainable
- Testable





Questions ?

